

Abstracts

A Two-Stage Monolithic Buffer Amplifier for 20 GHz Satellite Communication

W.C. Petersen and A.K. Gupta. "A Two-Stage Monolithic Buffer Amplifier for 20 GHz Satellite Communication." 1983 Microwave and Millimeter-Wave Monolithic Circuits Symposium Digest 83.1 (1983 [MCS]): 37-39.

Design, fabrication, and test results of a two-stage GaAs monolithic buffer amplifier for 20 GHz satellite communication are described in this paper. A gain of 13 ± 0.75 dB from 17.7 to 20.2 GHz was obtained from the 1.5×1.5 millimeter chip, which includes all necessary bias and dc blocking circuitry.

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